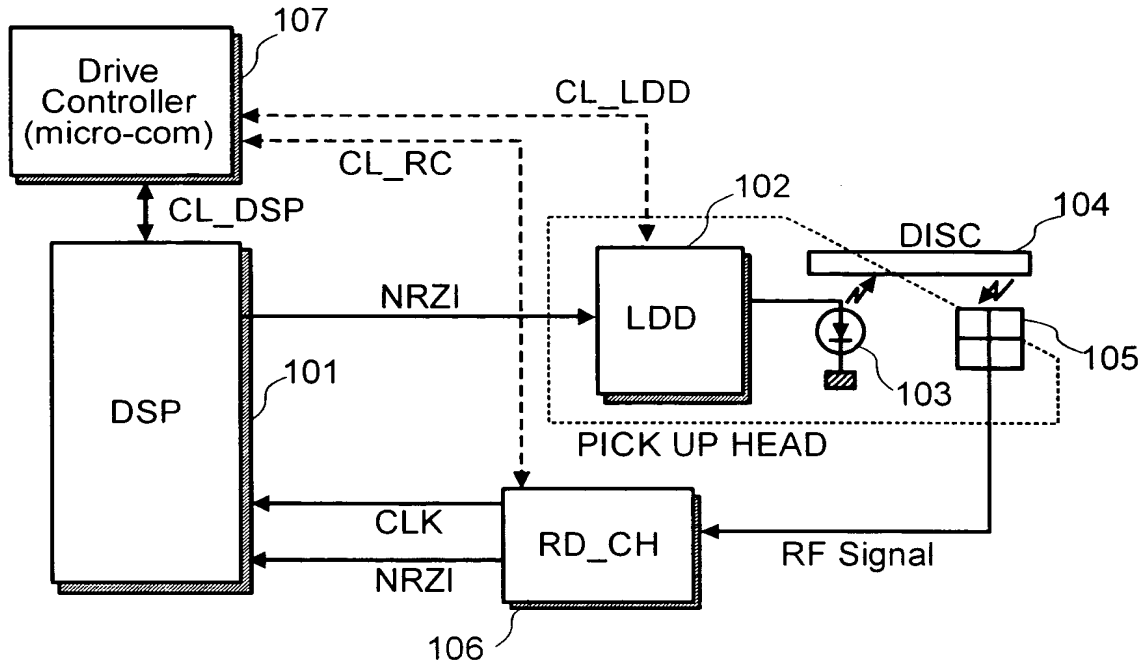
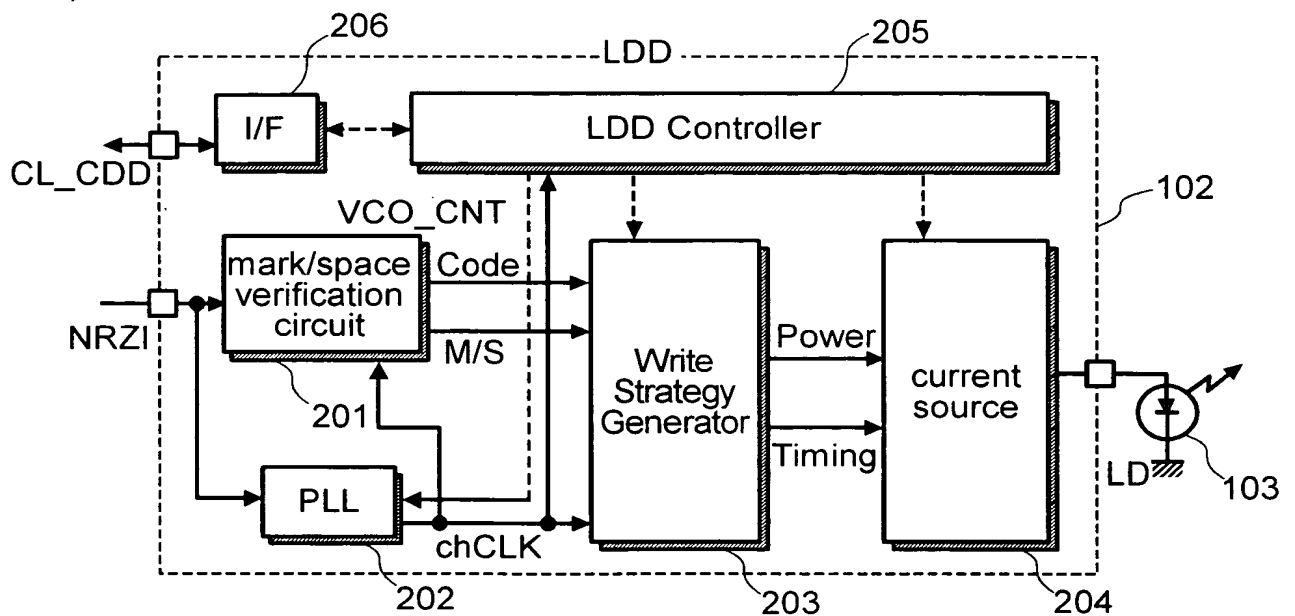


**FIG.1**



**FIG.2**



**FIG.3**

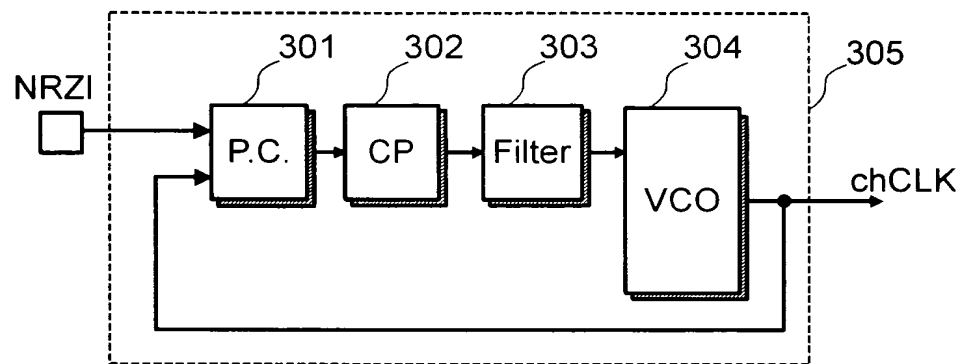
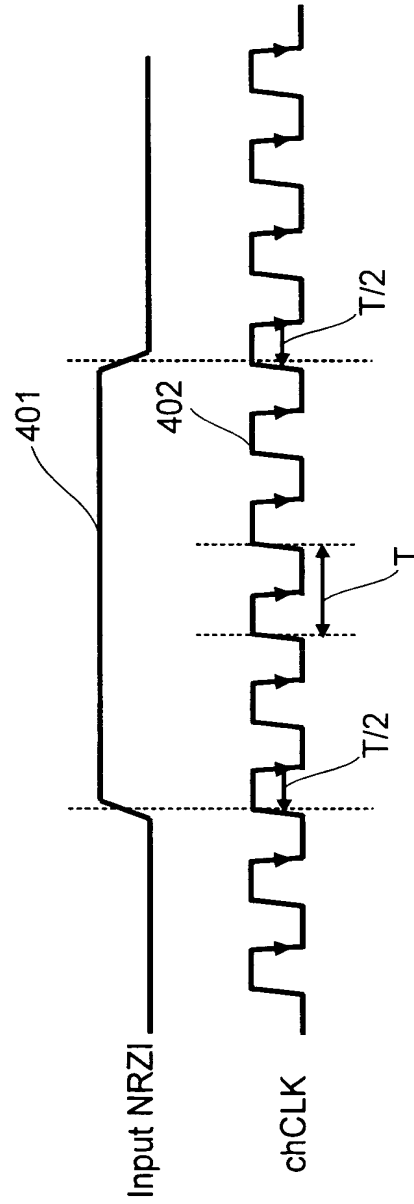
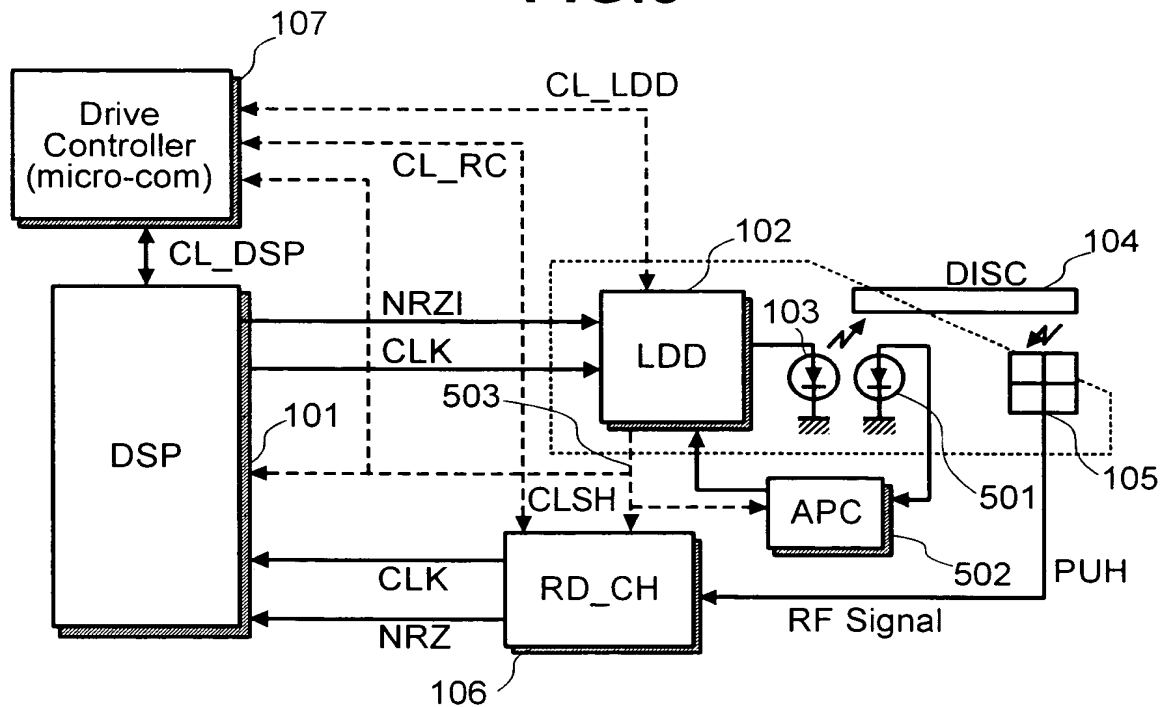


FIG. 4



**FIG.5**



**FIG.6**

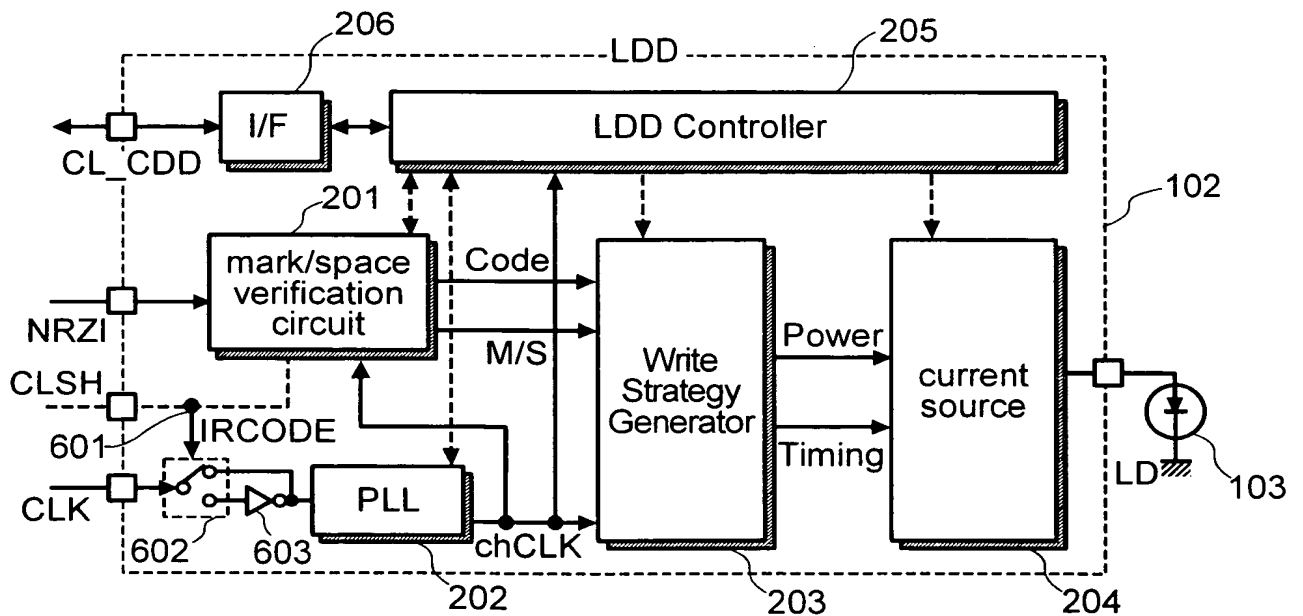


FIG. 6A

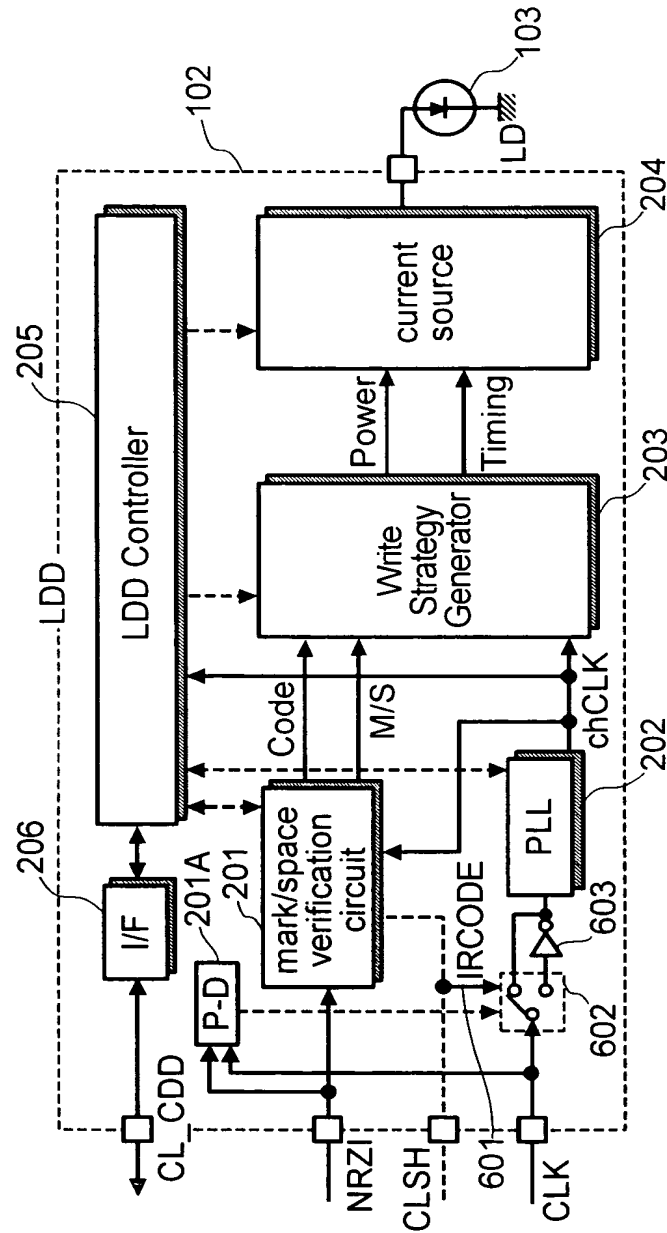


FIG. 7

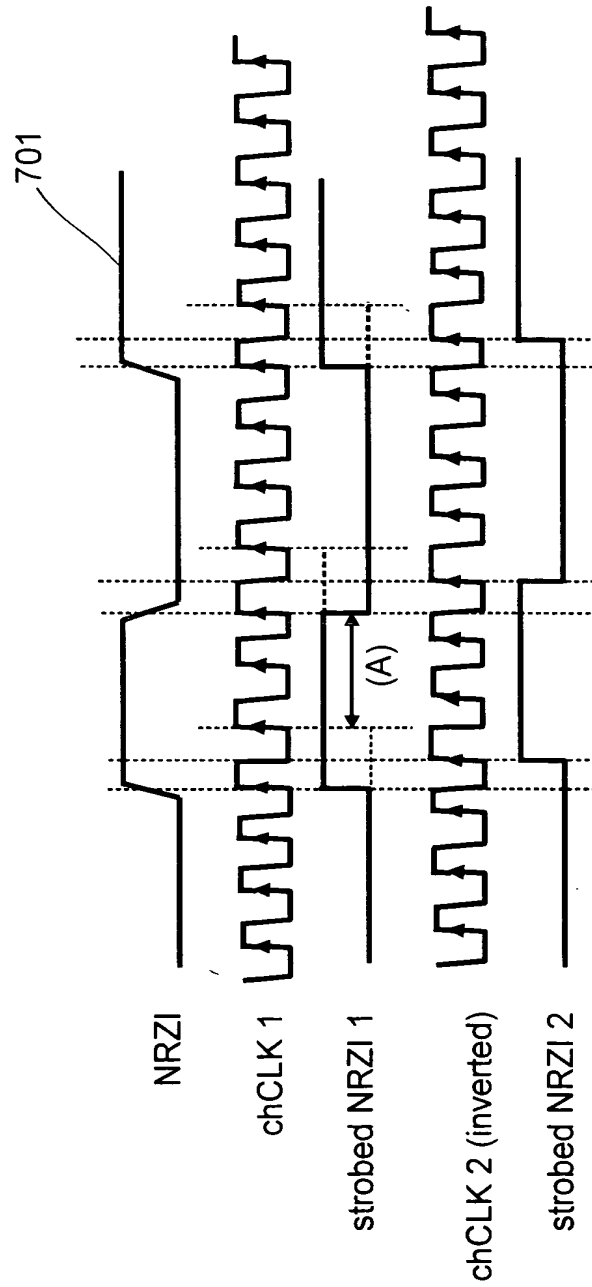
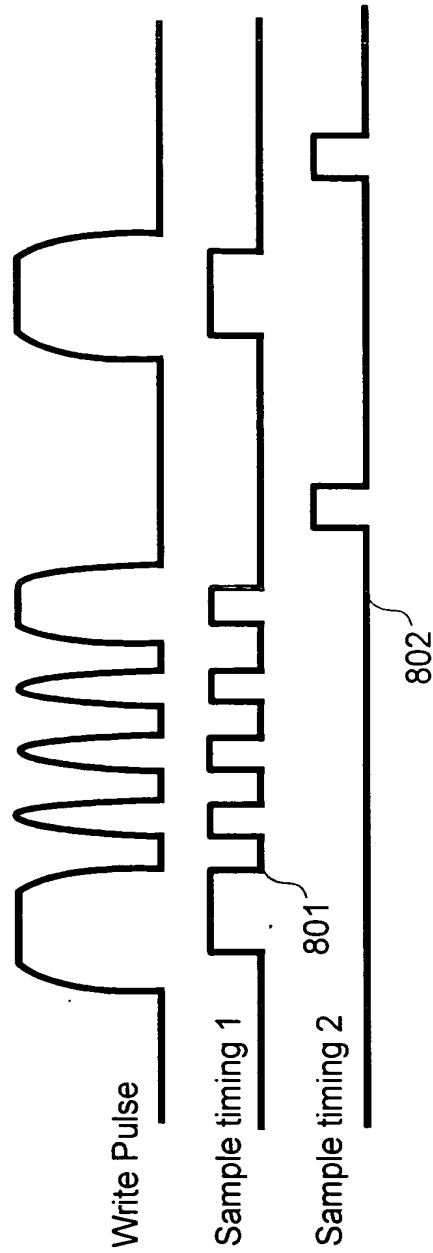


FIG.8



**FIG.9**

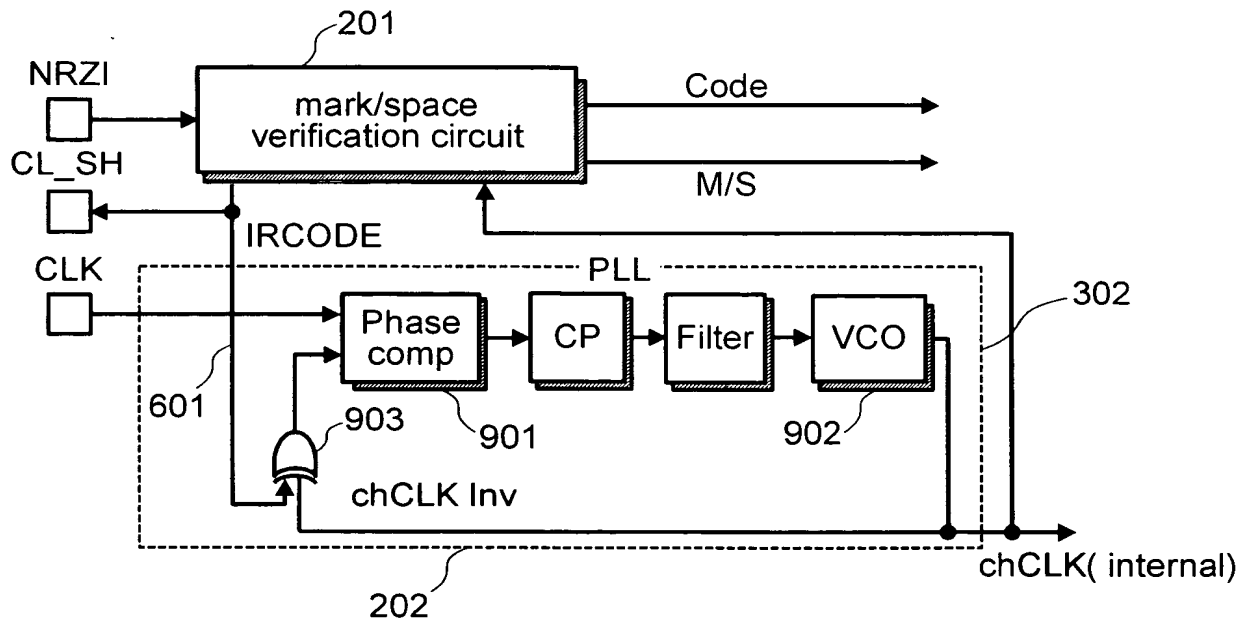




FIG.10

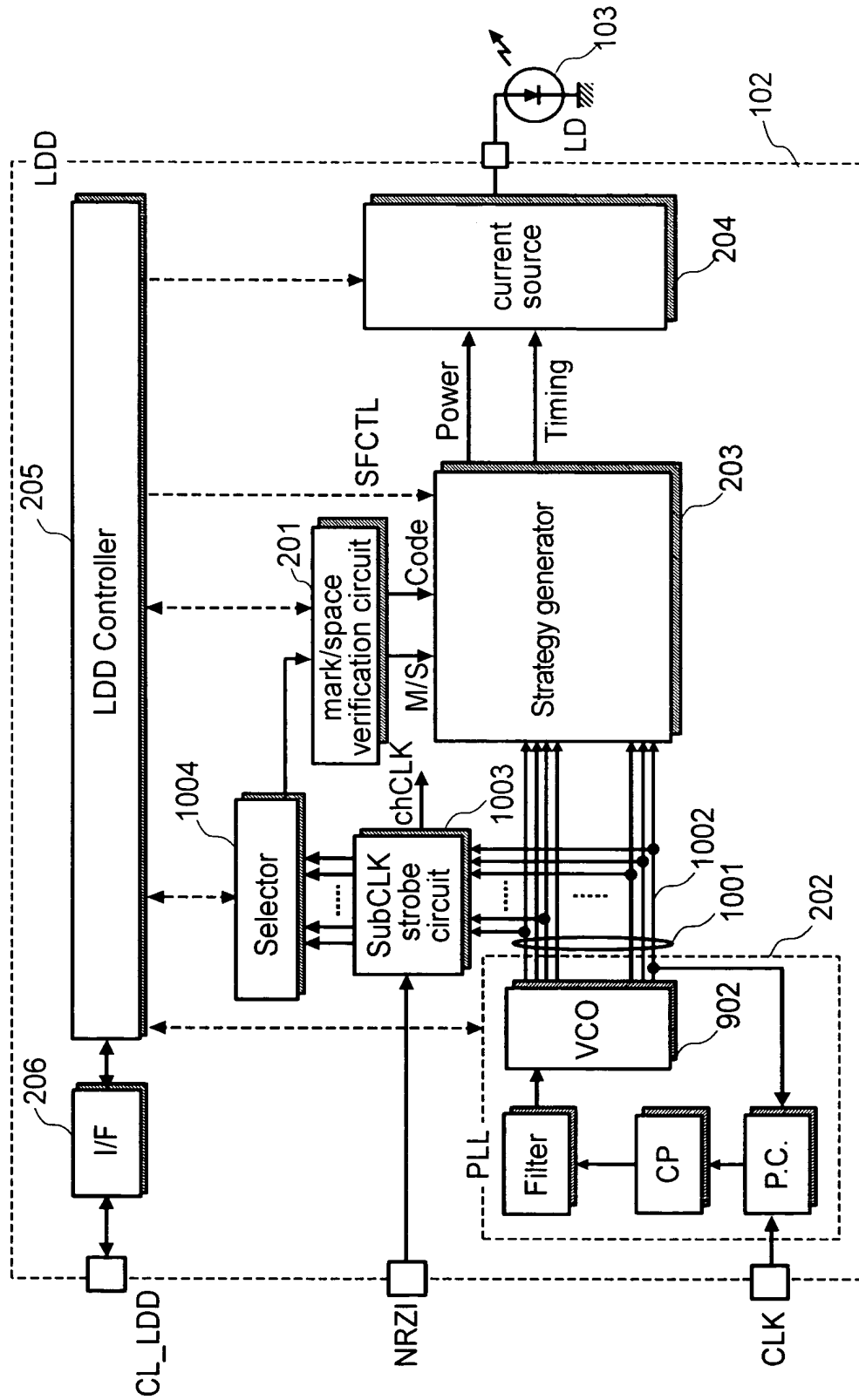
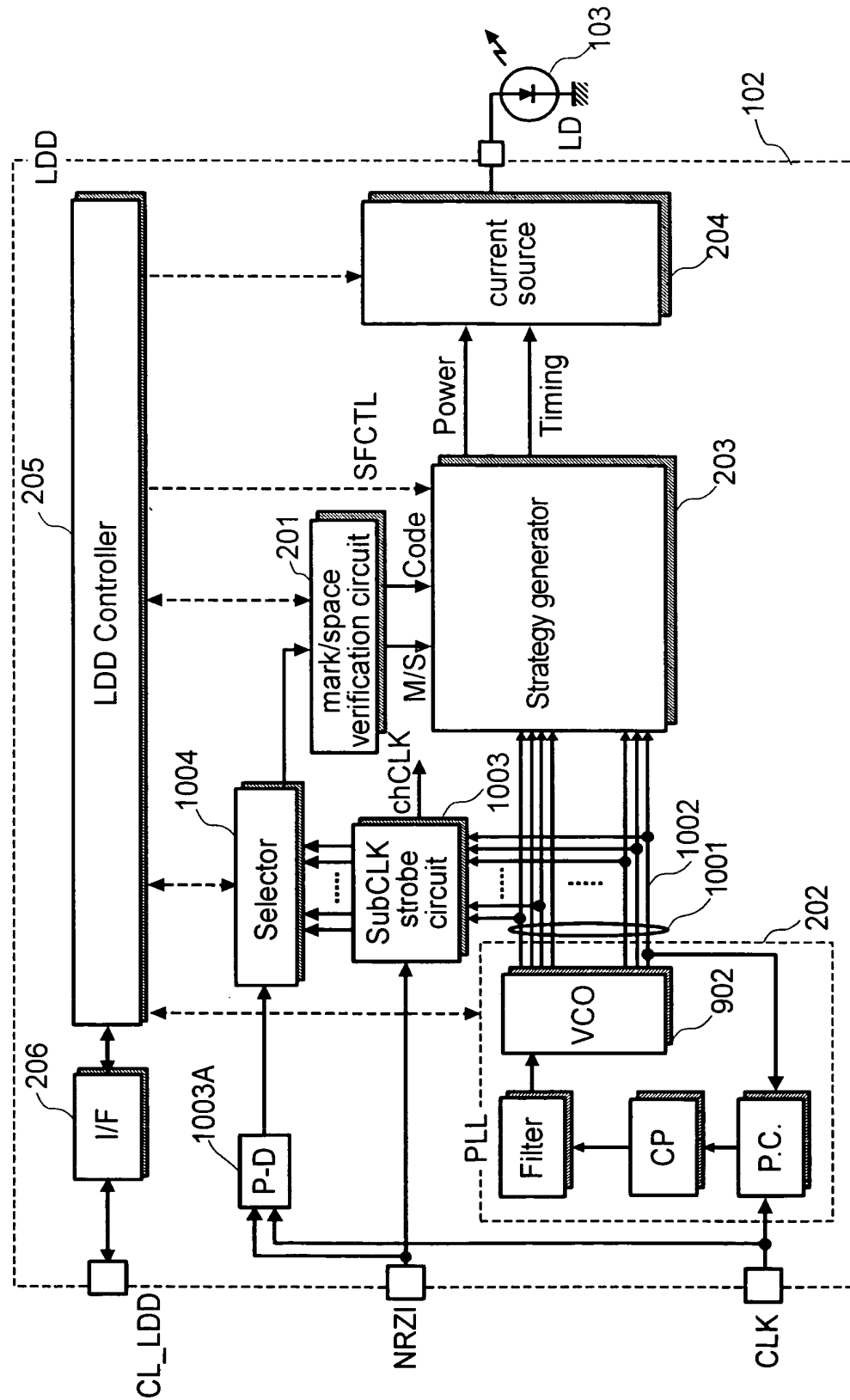


FIG. 10A



**FIG.11**

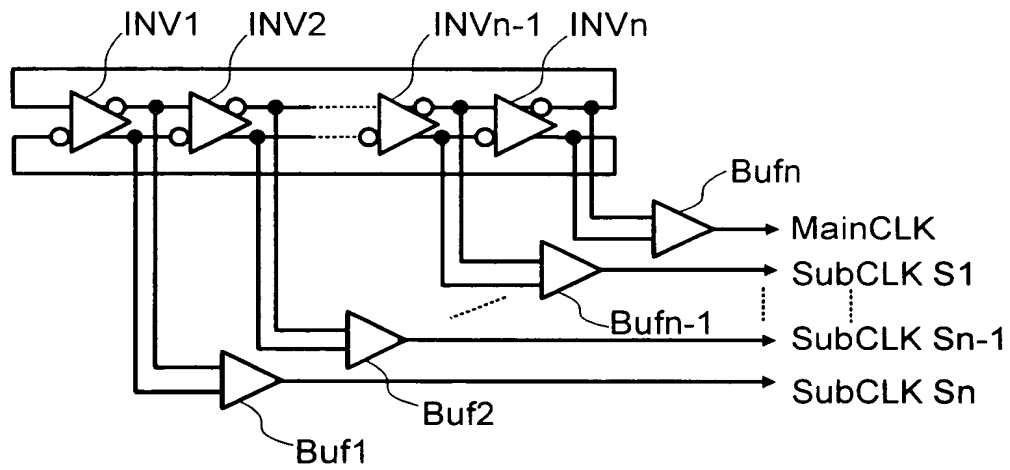


FIG.12

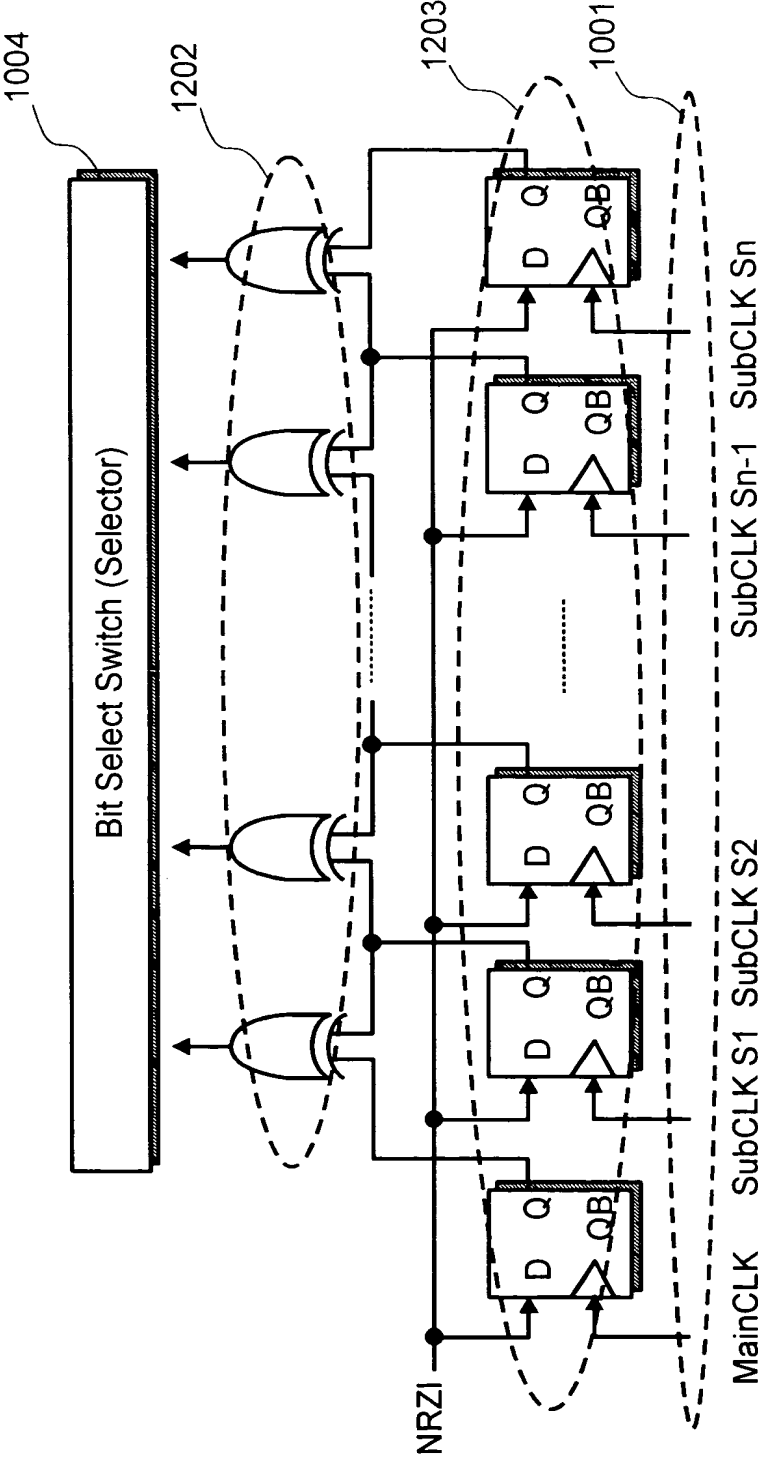


FIG.13

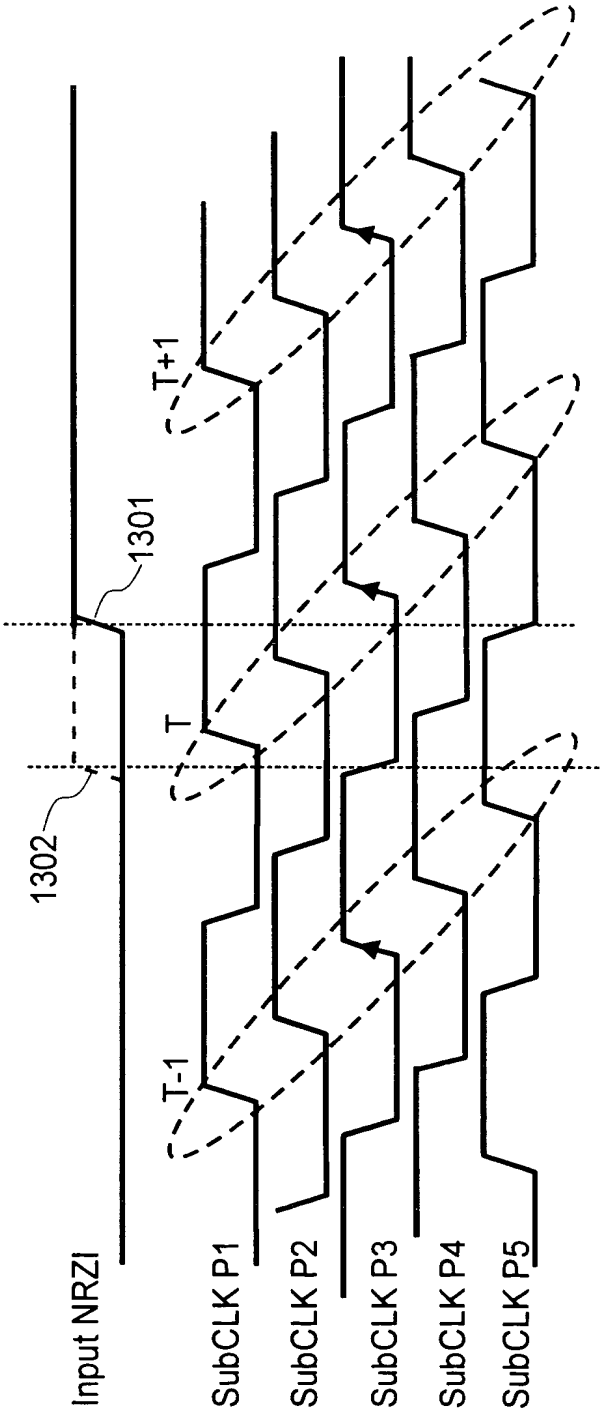


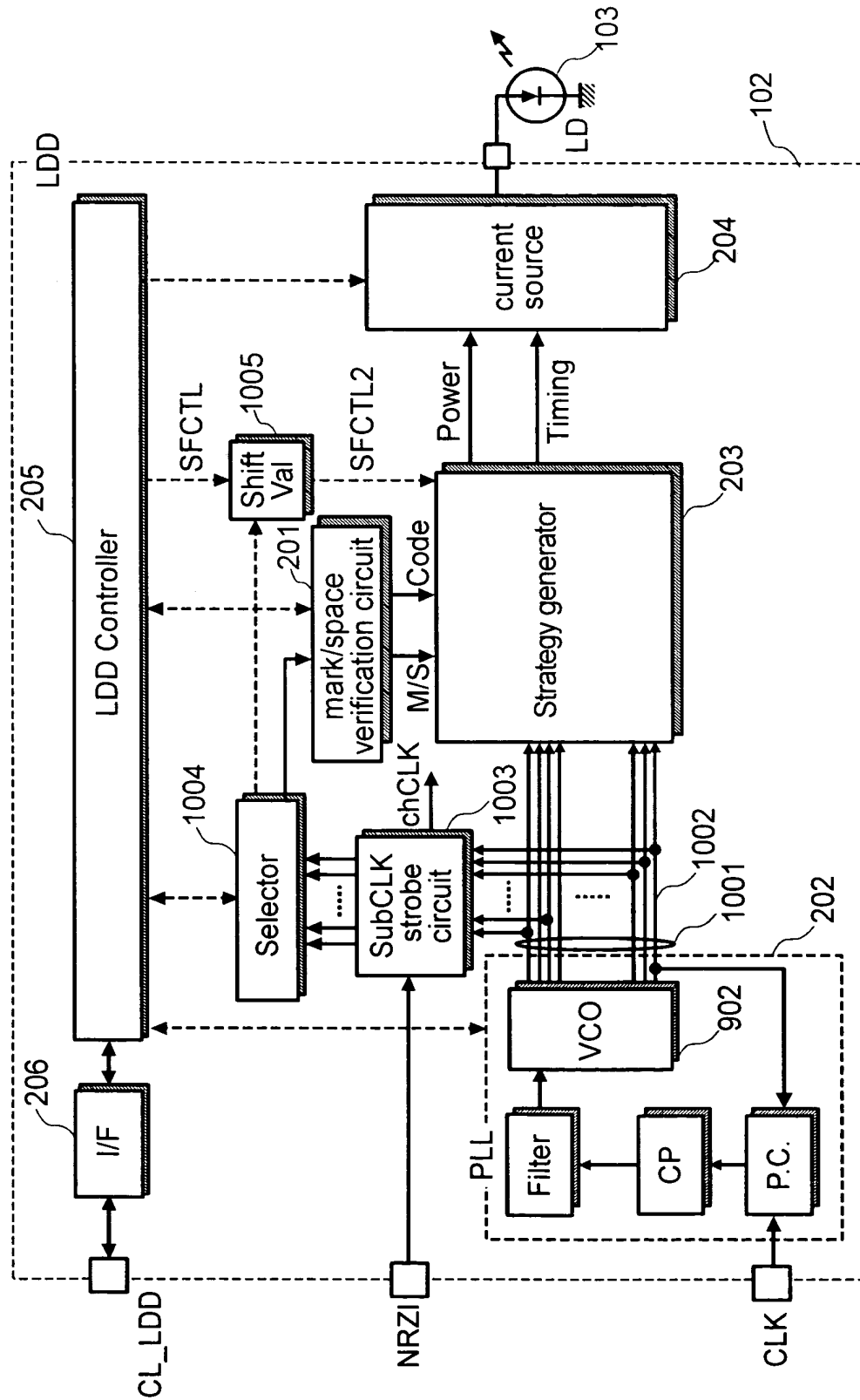
Table 2

SubCLK	...	T-1	T	T+1	...	chCLK
P1	...	0	1	1	...	x
P2	...	0	1	1	...	x
P3	...	0	1	1	...	○
P4	...	0	1	1	...	x
P5	...	0	1	1	...	x

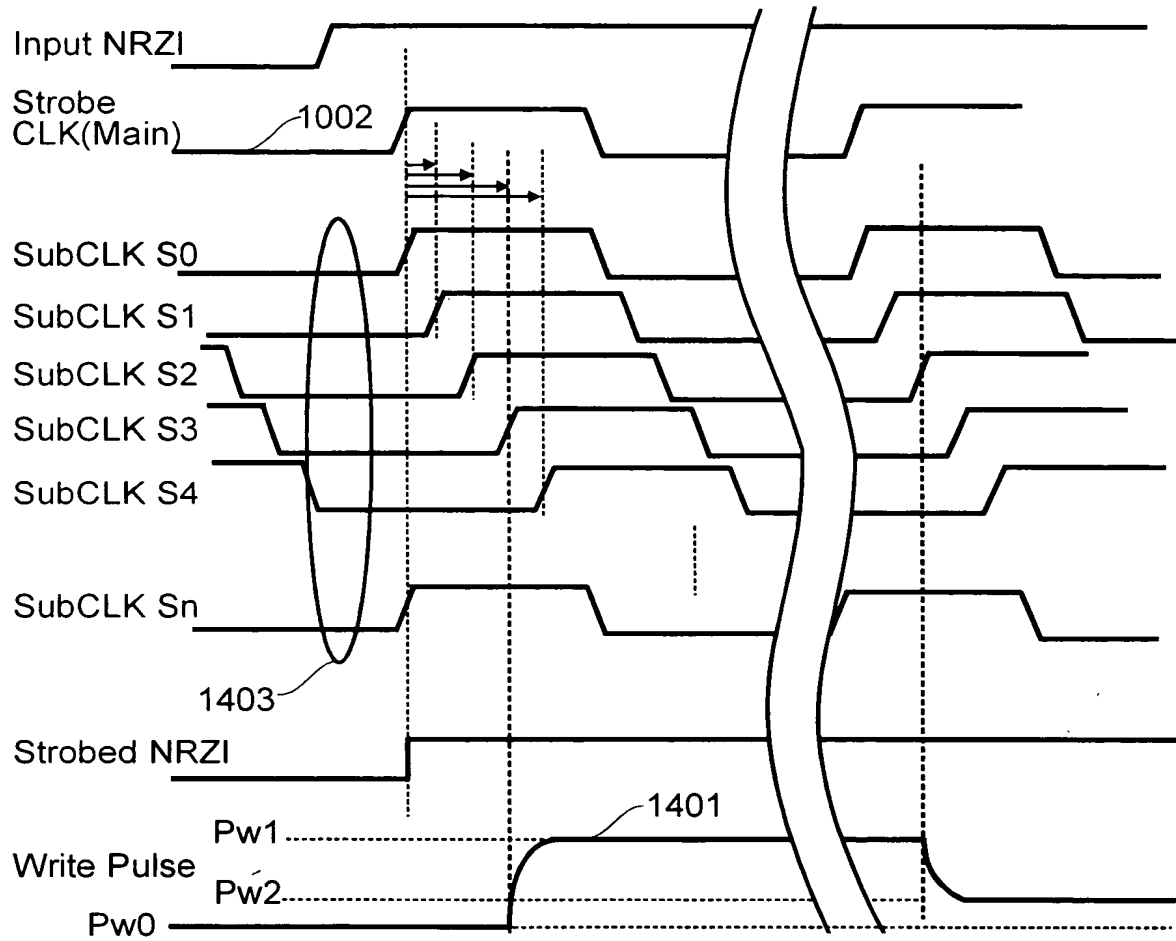
Table 1

SubCLK	...	T-1	T	T+1	...	chCLK
P1	...	0	0	1	...	x
P2	...	0	0	1	...	x
P3	...	0	1	1	...	x
P4	...	0	1	1	...	x
P5	...	0	1	1	...	○

FIG.14

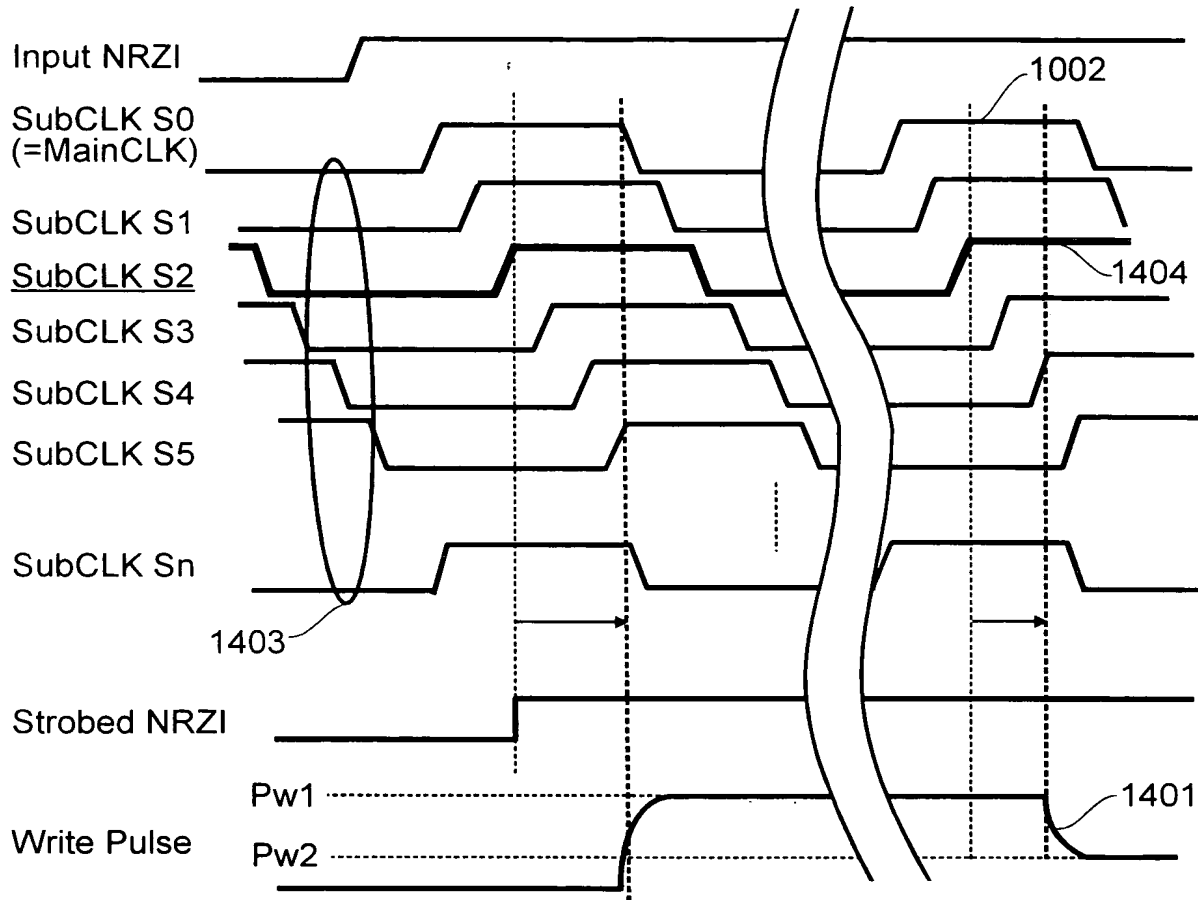


**FIG.15**



Register	TPw1	TPw2
Target Power	Pw1	Pw2
Setting Shift	#3	#2
Real Shift	S3	S2

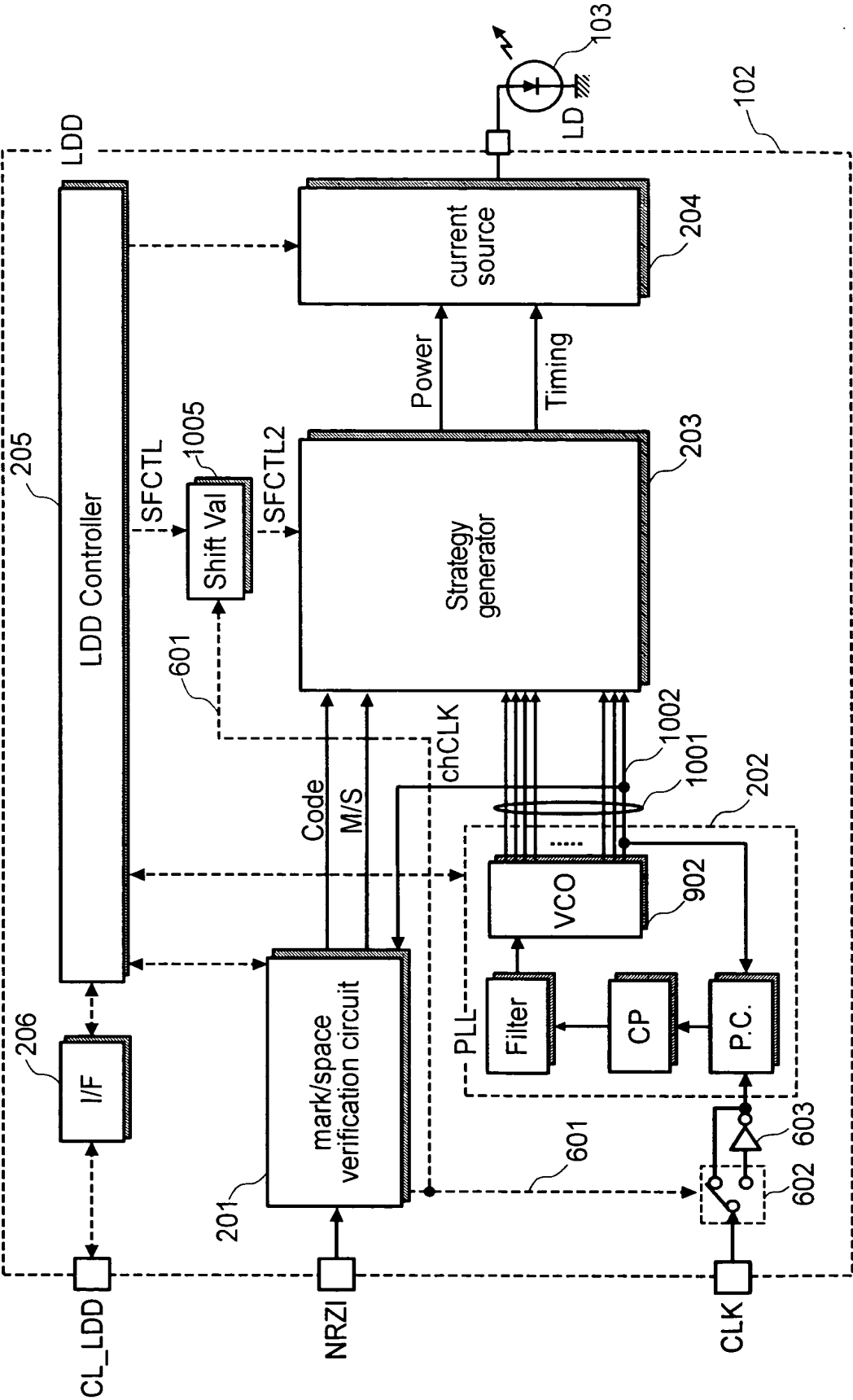
**FIG.16**



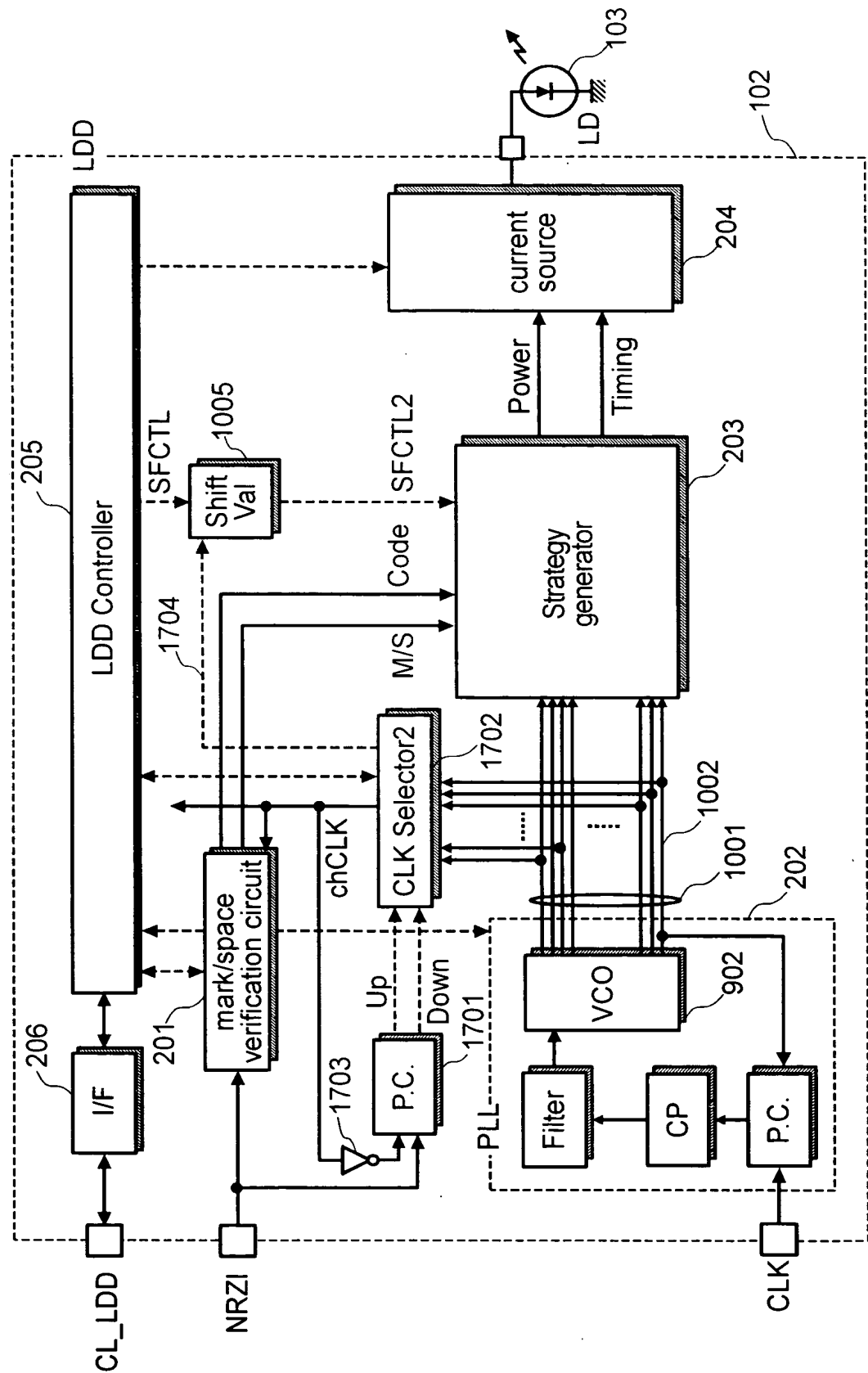
Register	TPw1	TPw2
Target Power	Pw1	Pw2
Setting Shift	#3	#2
chCLK Shift	S2	
Real Shift	S5(=#3+2)	S4(=#2+2)



FIG.17



**FIG. 18**



**FIG.19**

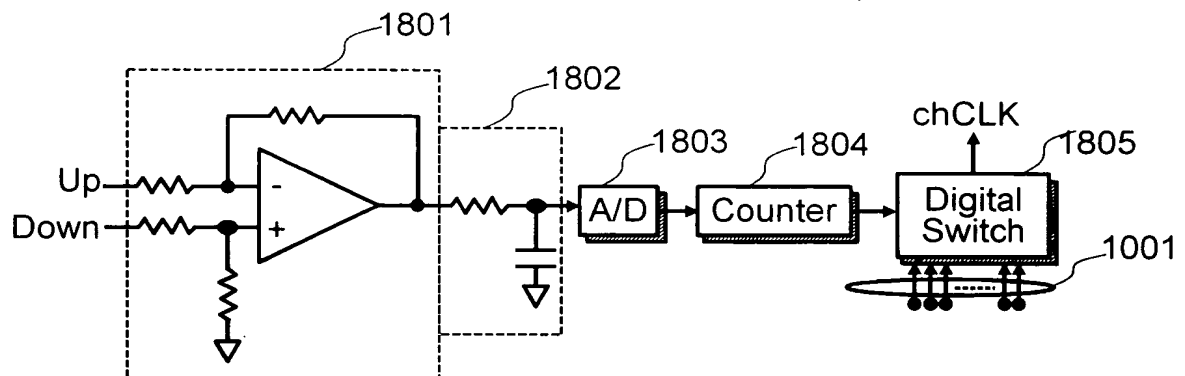


FIG. 20

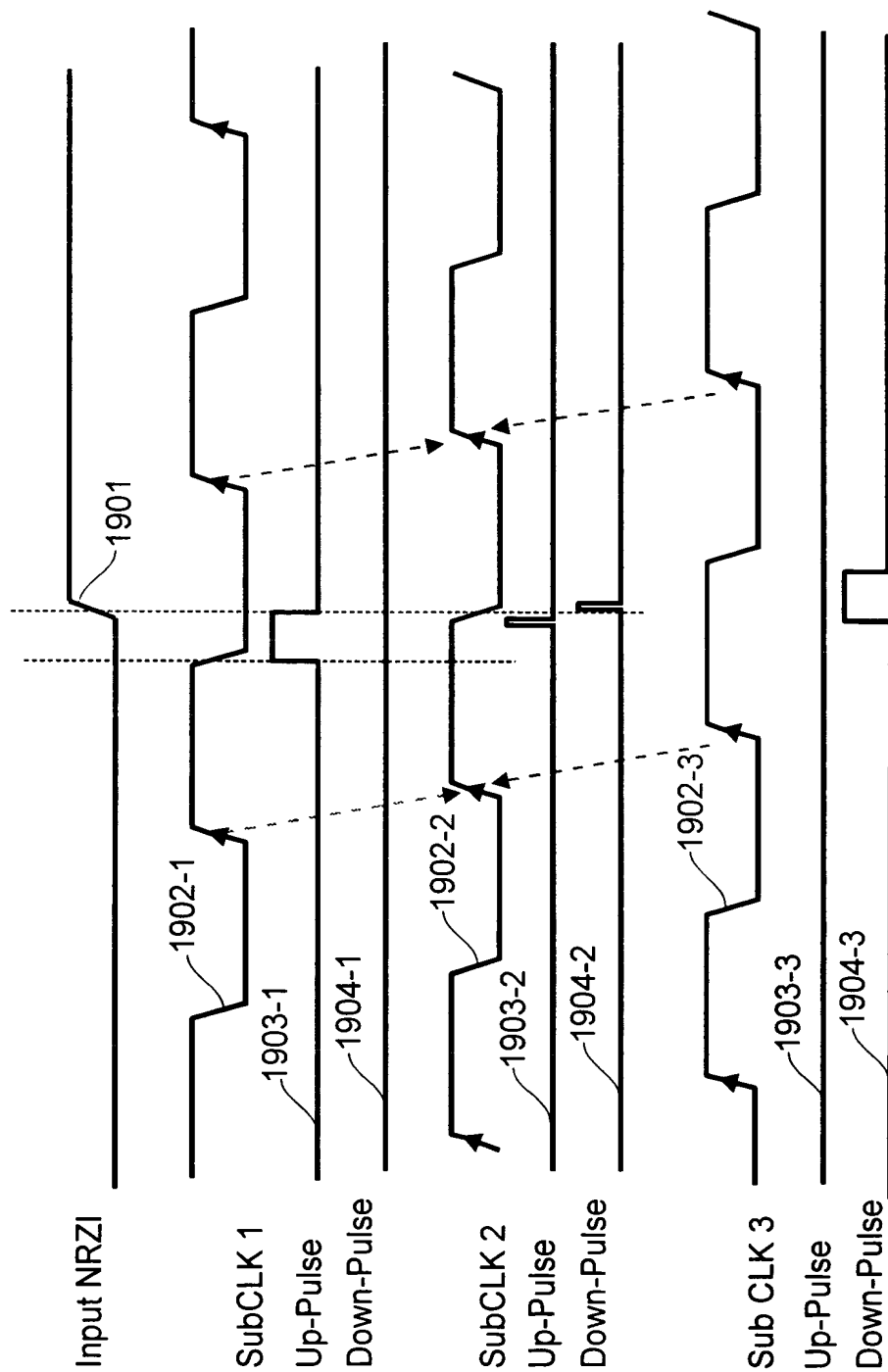


FIG. 21

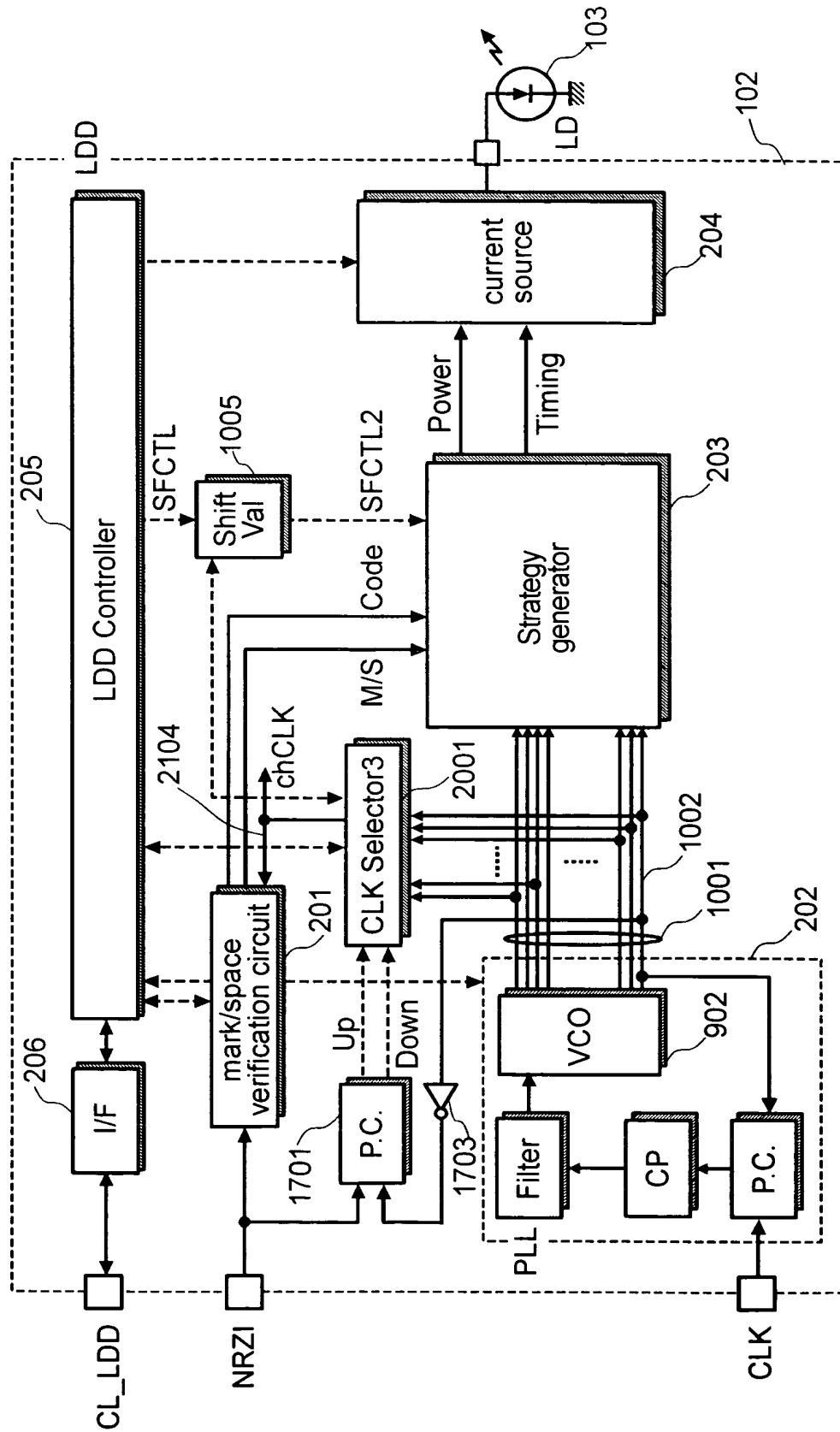
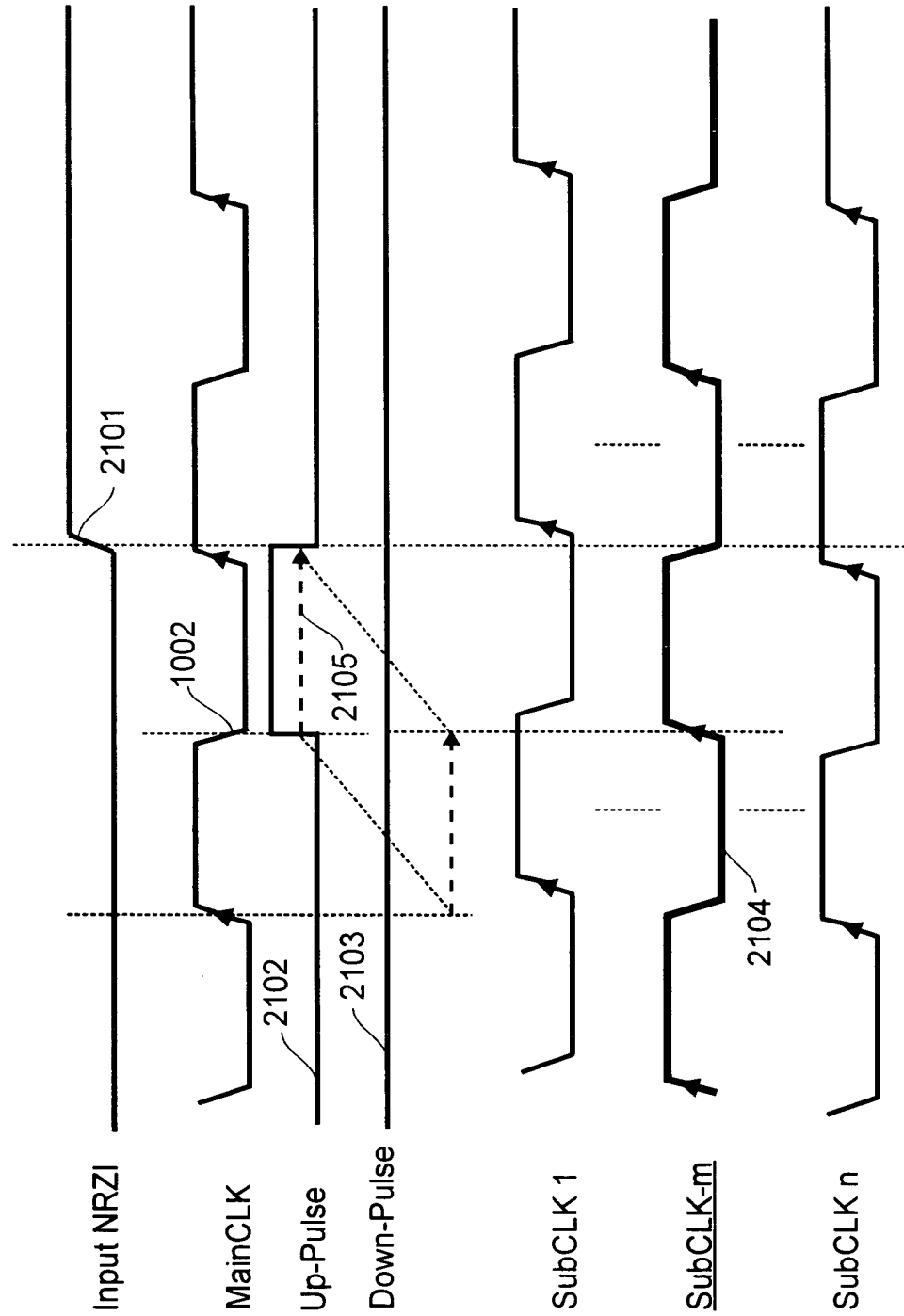


FIG. 22



**FIG. 23**

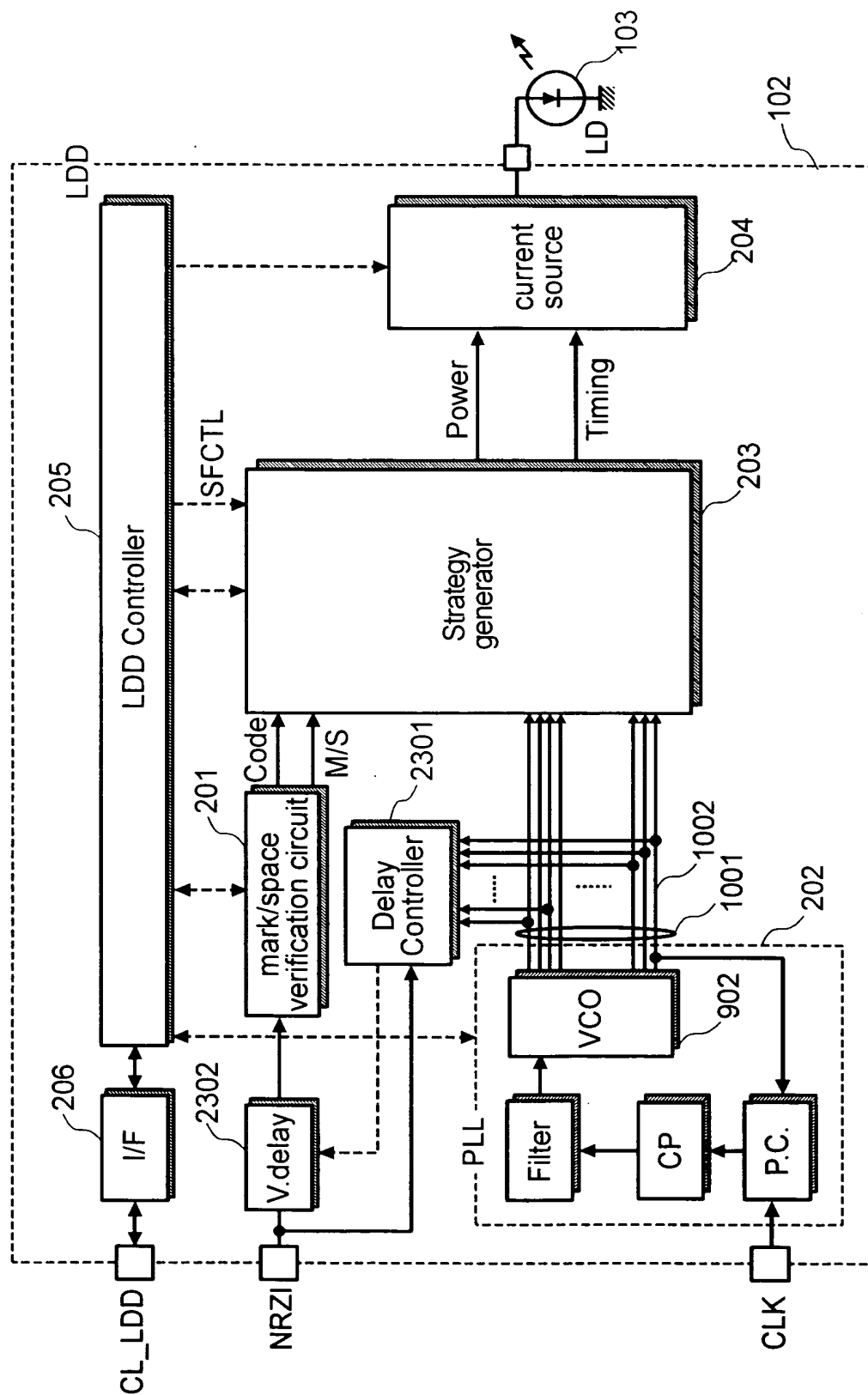


FIG.23A

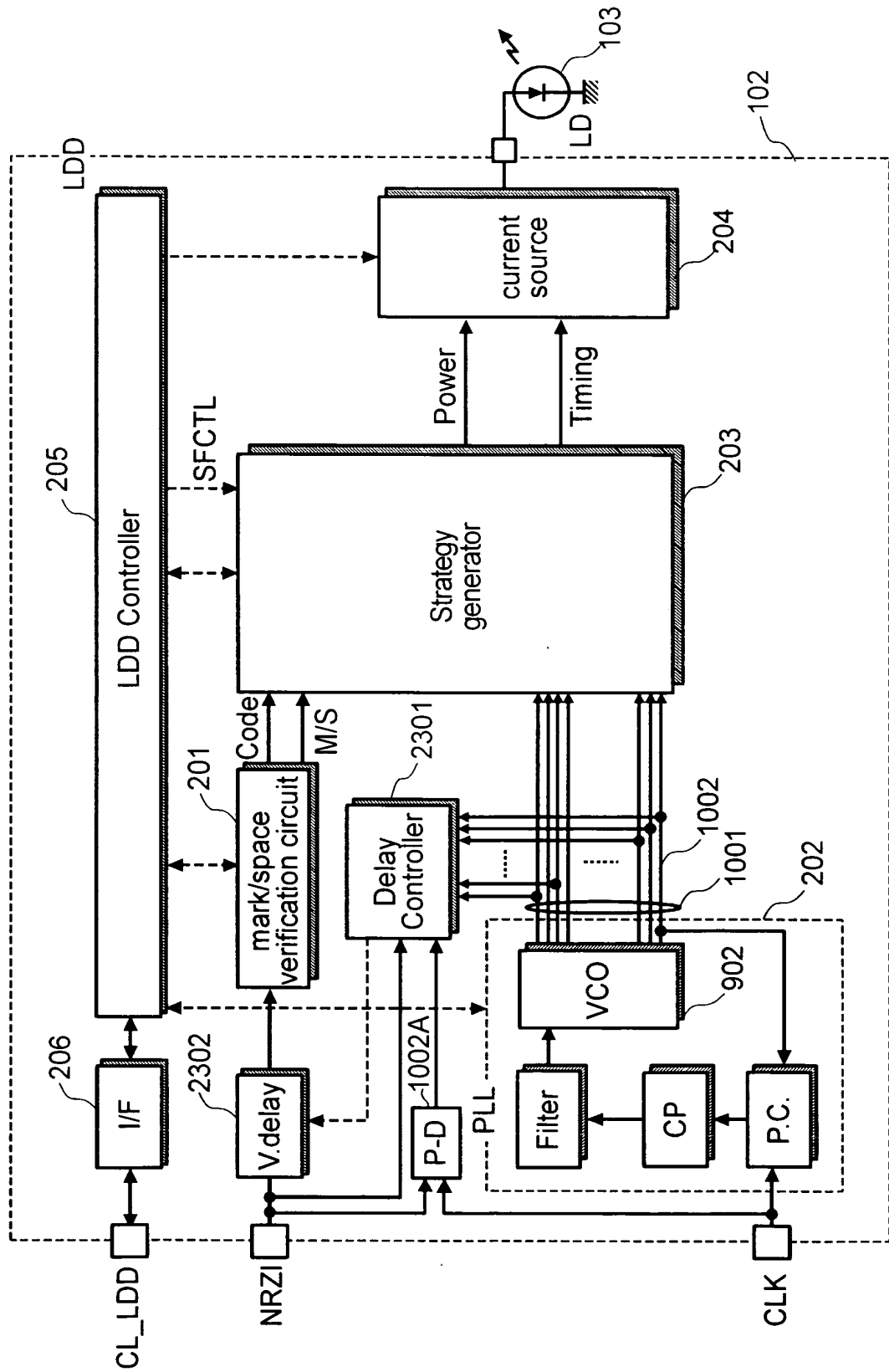




FIG.24

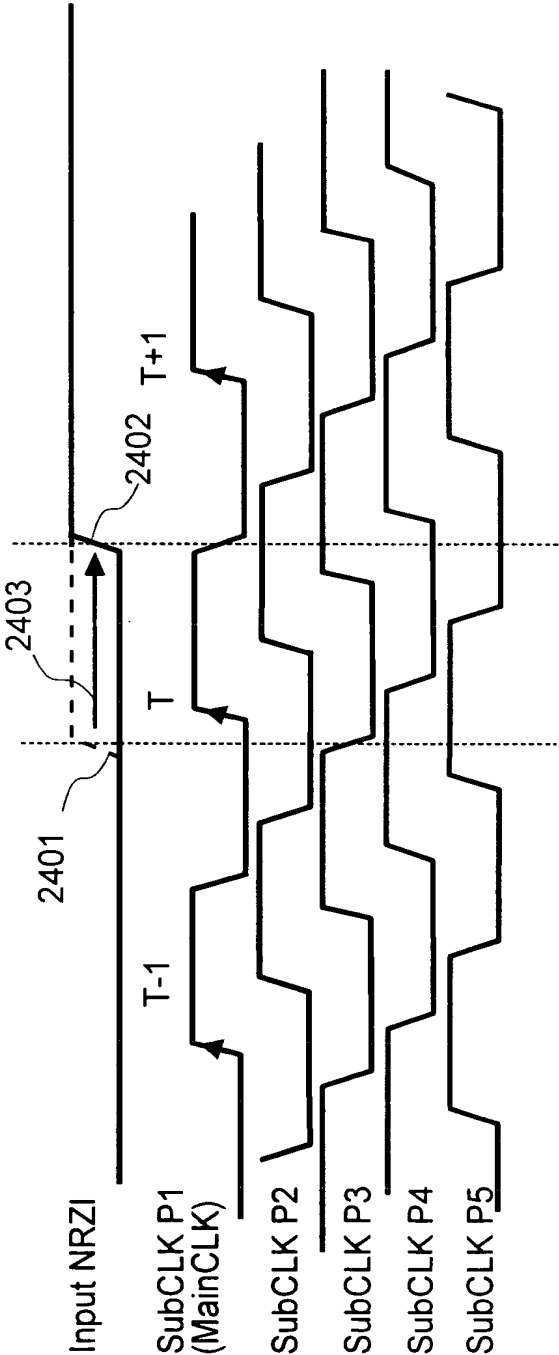


Table 4

SubCLK	...	T-1	T	T+1	...
P1	...	0	0	1	...
P2	...	0	0	1	...
P3	...	0	0	1	...
P4	...	0	1	1	...
P5	...	0	1	1	...

Table 3

SubCLK	...	T-1	T	T+1	...
P1	...	0	1	1	...
P2	...	0	1	1	...
P3	...	0	1	1	...
P4	...	0	1	1	...
P5	...	0	1	1	...